

FIG. 1

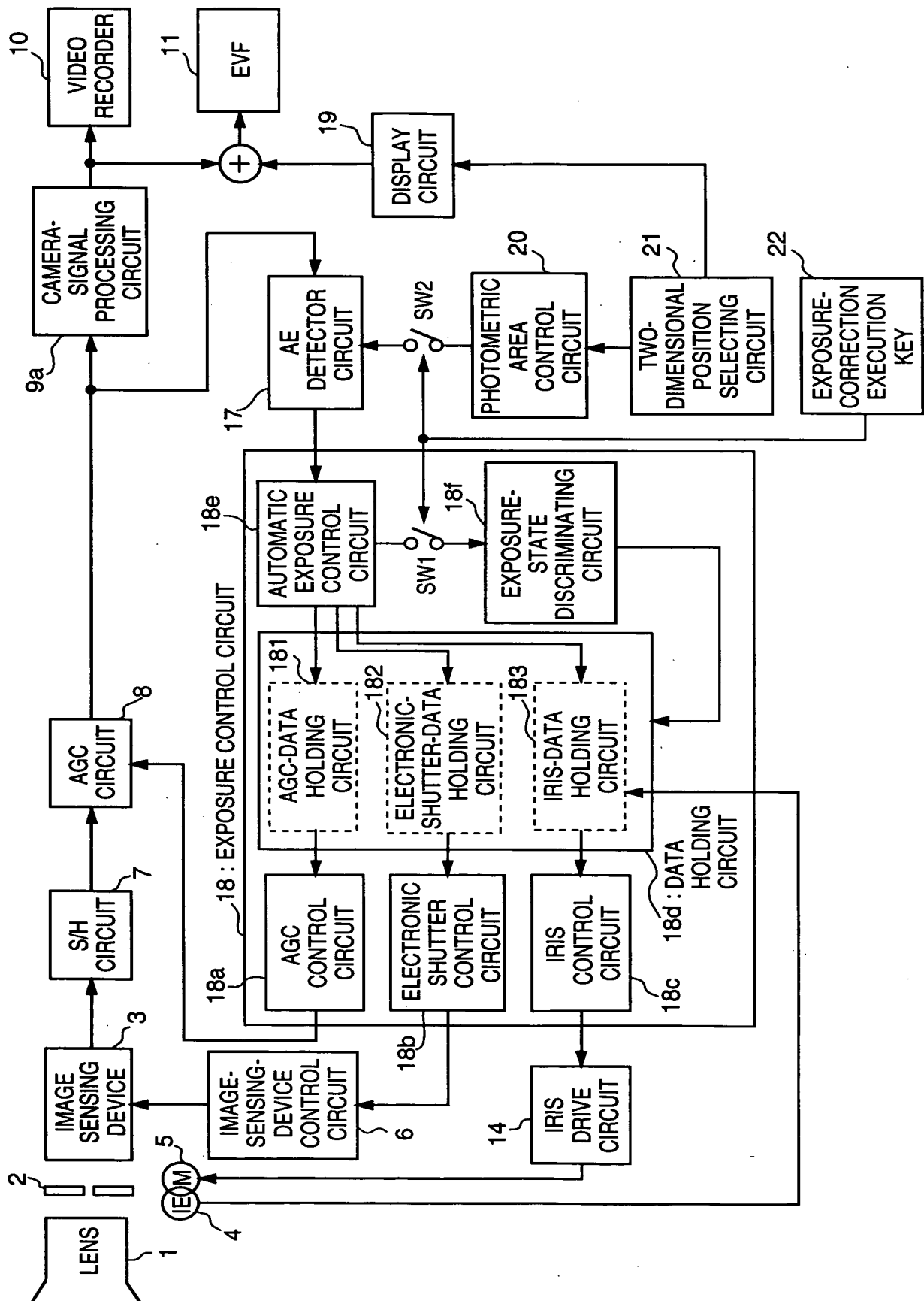


FIG. 2

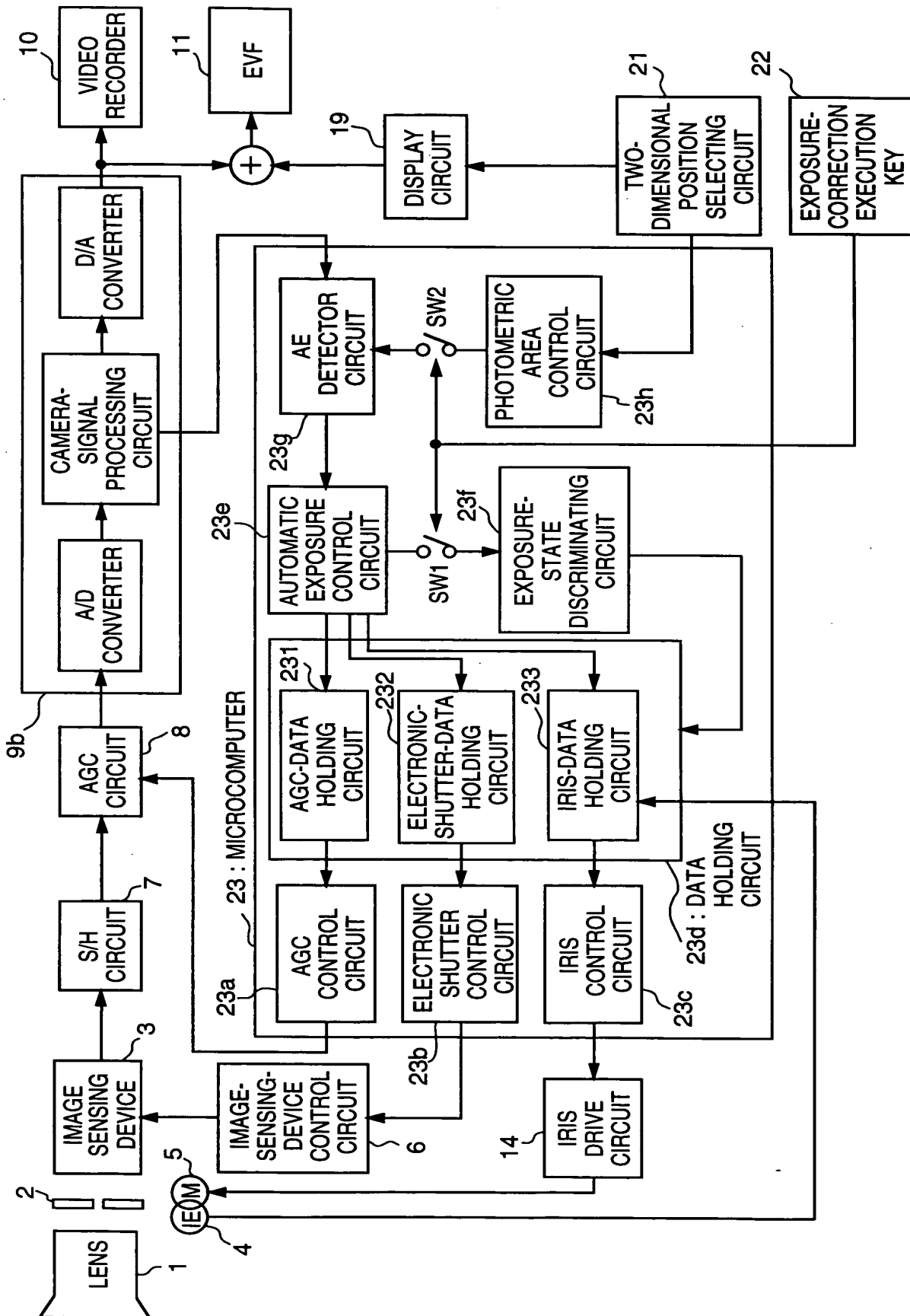


FIG. 3

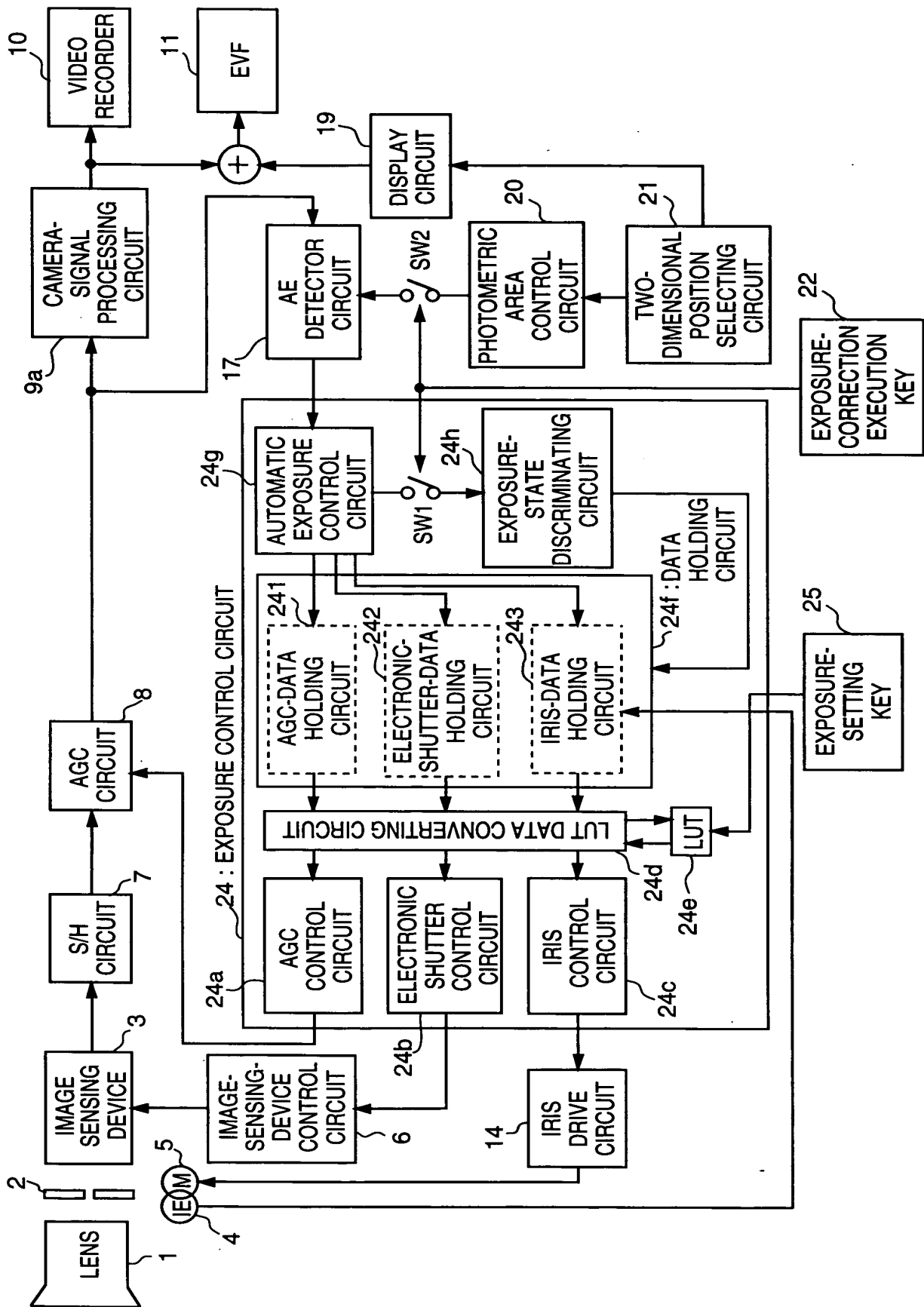


FIG. 4

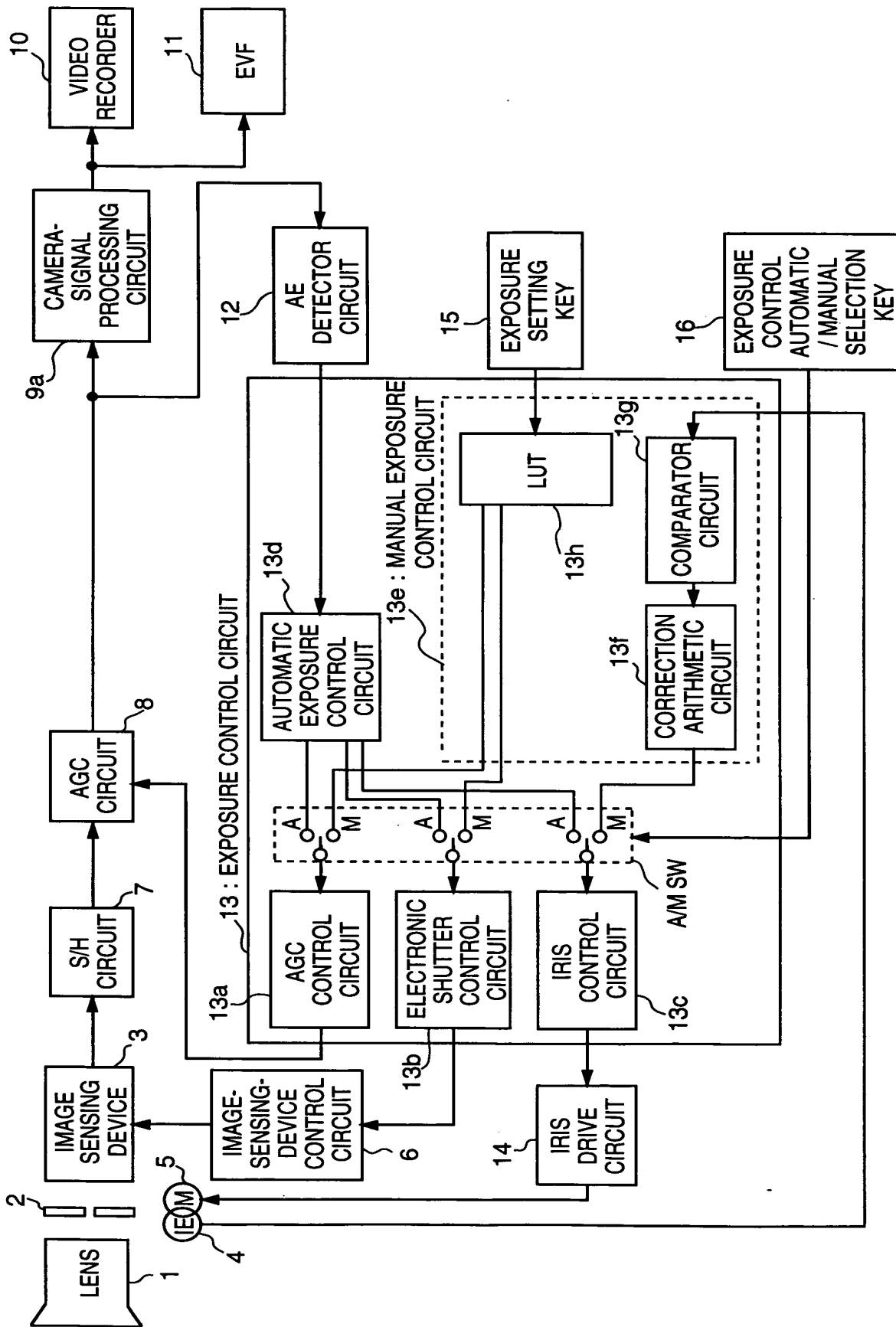


FIG. 5

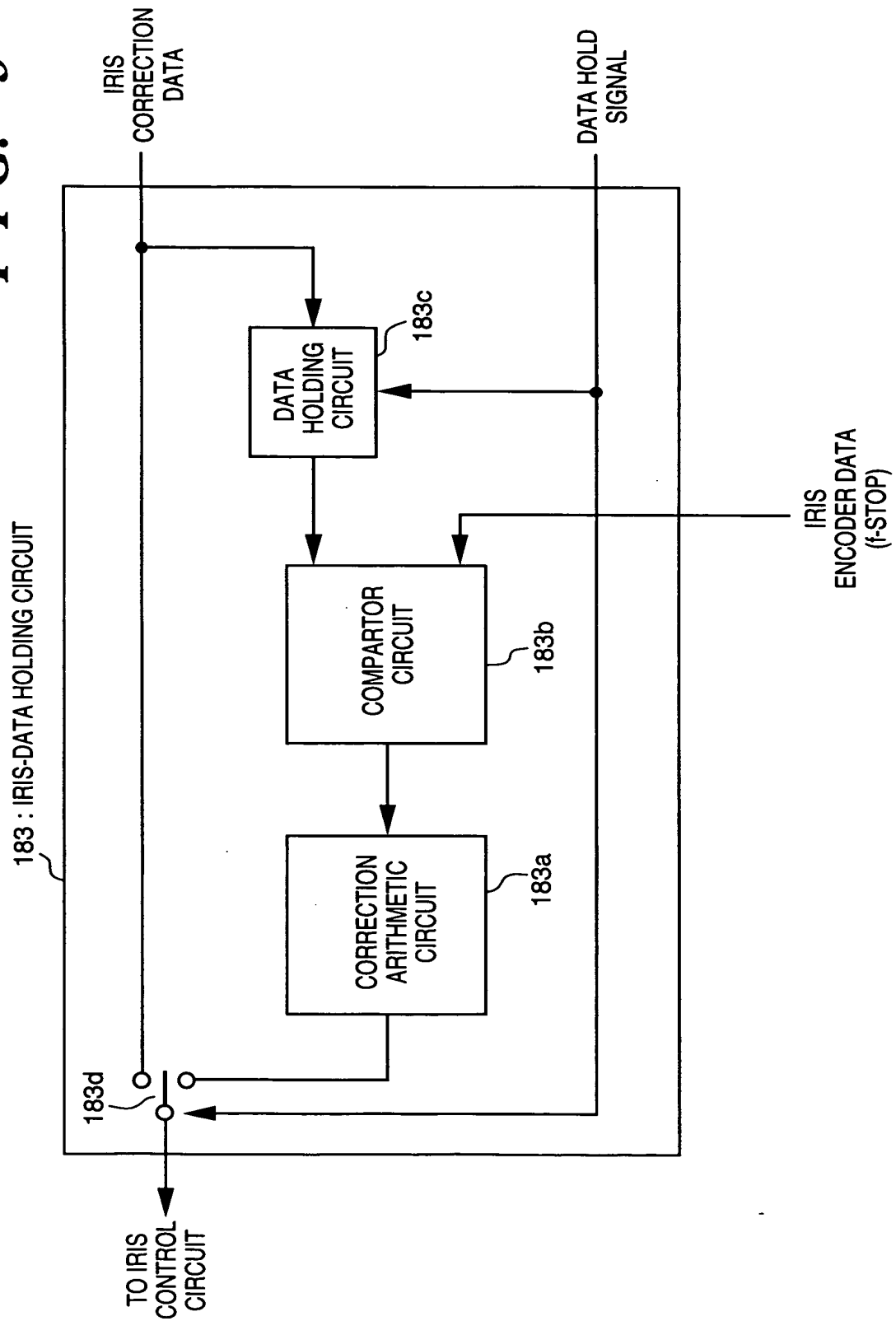


FIG. 6

① LUT EXAMPLE 1

DATA No.	IRIS DATA
data1	OPEN
data2	F2.8
data3	F4
data4	F5.6
data5	F8
data6	F11
data7	F16
data8	F22
data9	F32
data10	CLOSE

② LUT EXAMPLE 2

DATA No.	IRIS DATA	AGC DATA	SHUTTER DATA
data1	OPEN	MAX	1/60
data2		18dB	
data3		12dB	
data4		6dB	
data5		0dB	
data6	F2.8		
data7	F4		
data8	F5.6		
data9	F8		
data10	F11		
data11	F22		
data12	F32		
data13			1/100
data14			1/250
data15			1/500
data16			1/1000
data17	CLOSE		

FIG. 7A

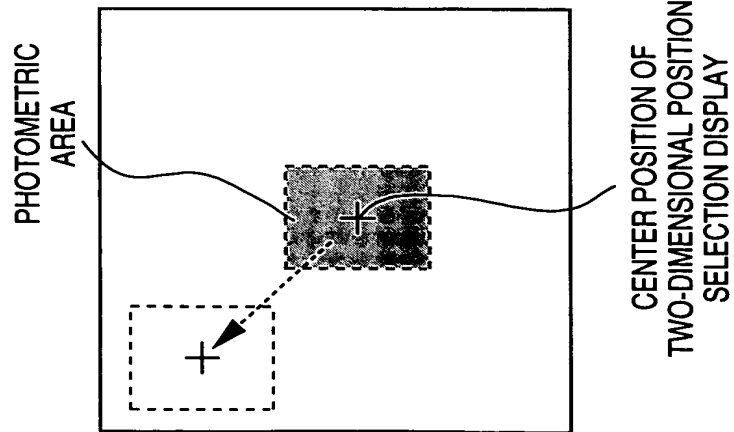


FIG. 7B

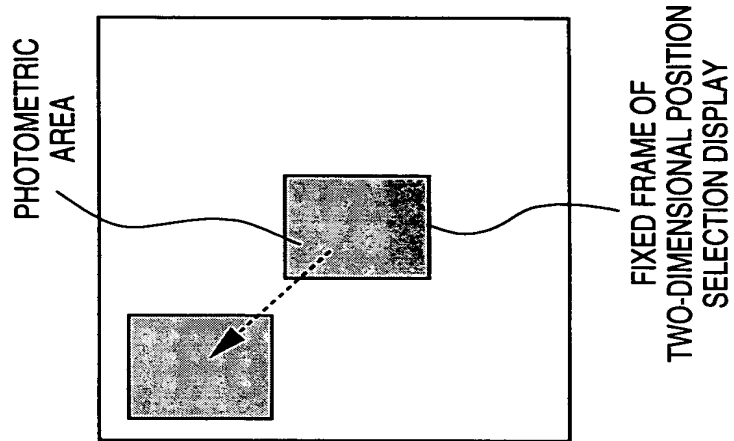


FIG. 7C

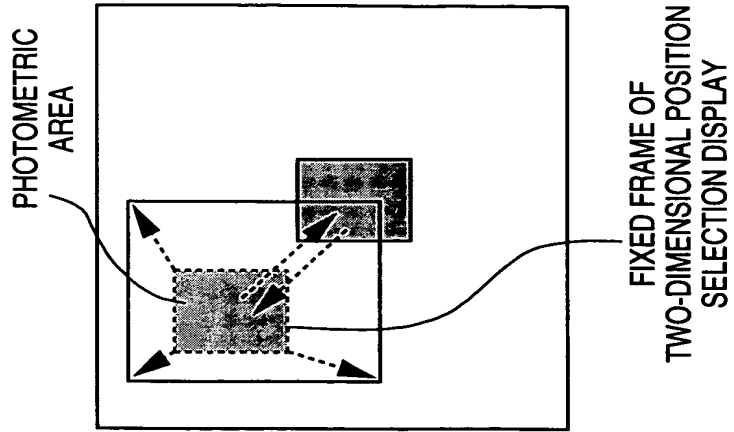


FIG. 8

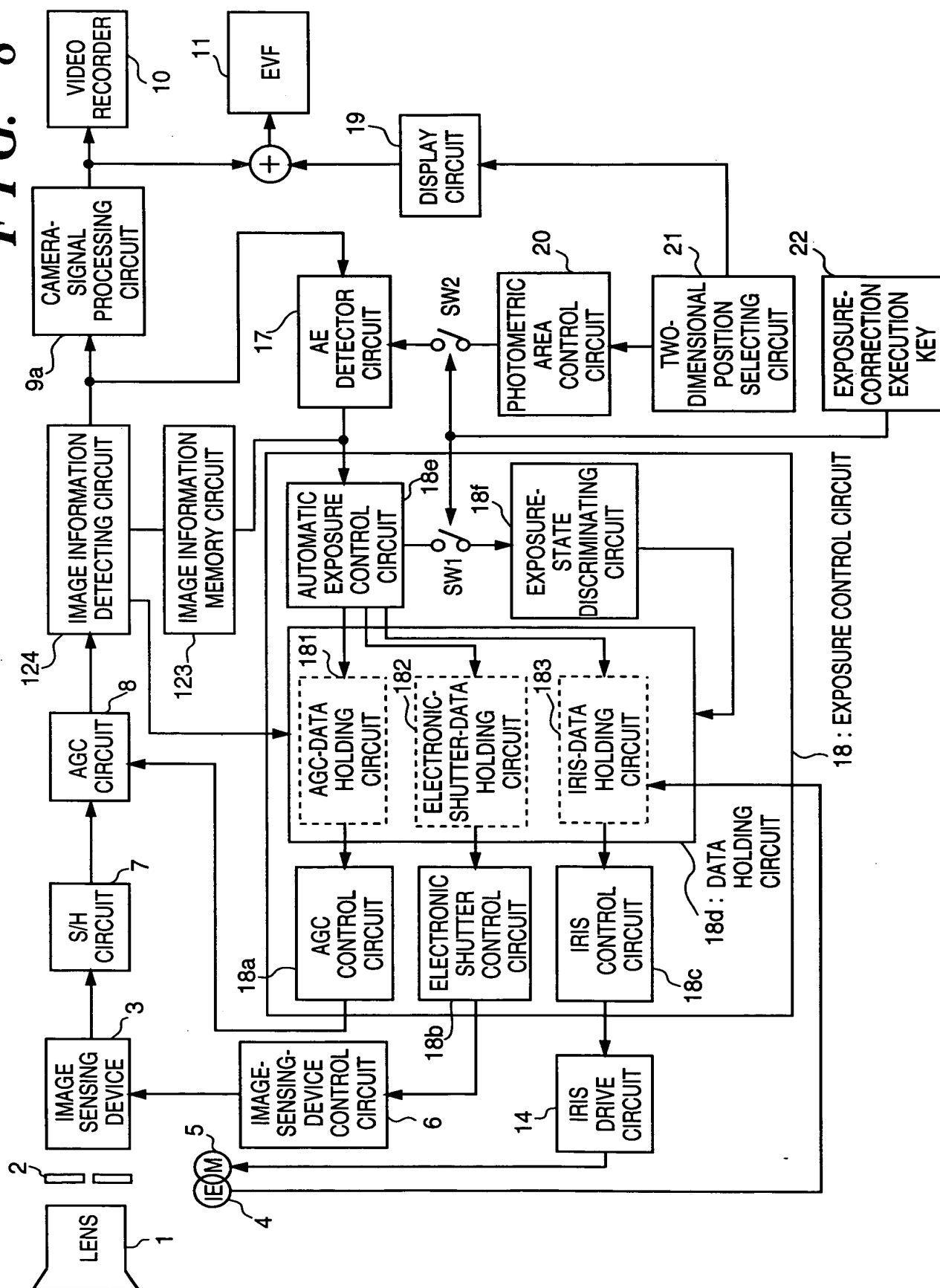


FIG. 9

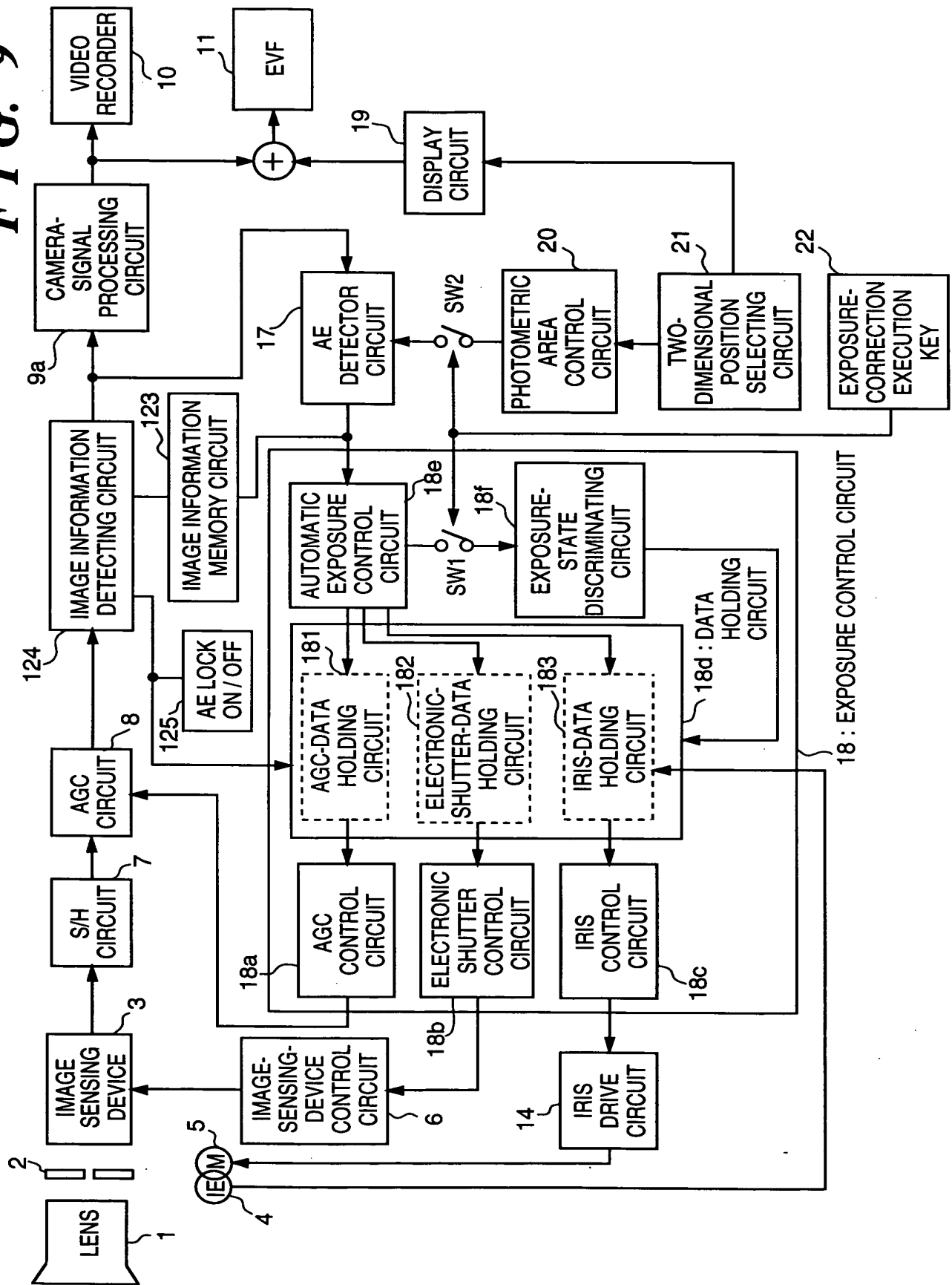
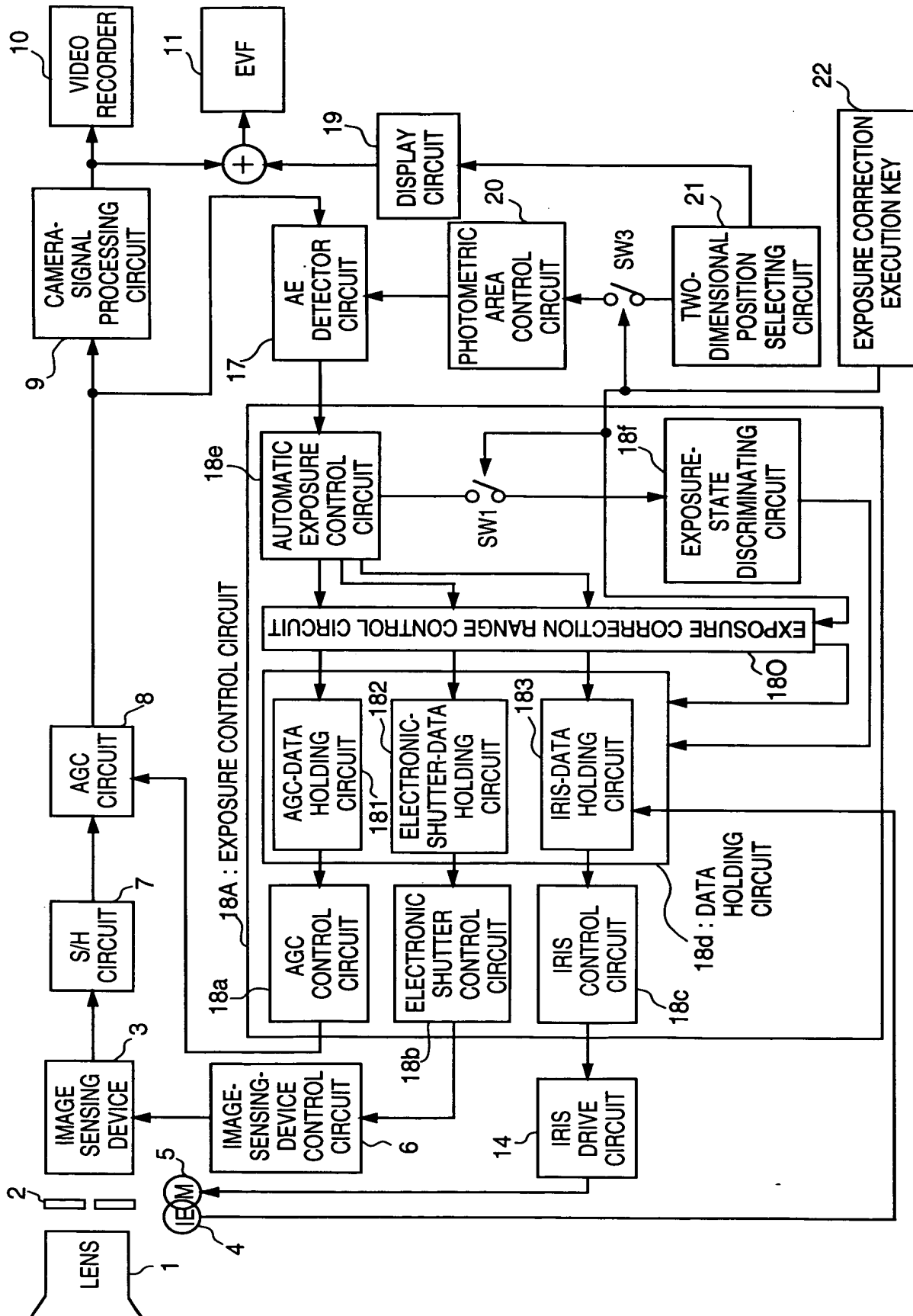


FIG. 10



The diagram illustrates a video camera system with the following components and connections:

- Input Stage:** A **LENS** (1) is connected to an **IMAGE SENSING DEVICE** (3) via an **IEOM** (4). The image sensing device is also connected to an **IMAGE-SENSING-DEVICE CONTROL CIRCUIT** (6).
- Signal Processing Path:** The image sensing device (3) outputs to an **S/H CIRCUIT** (7), which connects to an **AGC CIRCUIT** (8). The AGC circuit (8) connects to an **A/D CONVERTER** (109a), which then connects to a **CAMERA-SIGNAL PROCESSING CIRCUIT** (109b). The processing circuit (109b) connects to a **D/A CONVERTER** (109c), which outputs to a **VIDEO RECORDER** (10).
- Microcomputer (23A):** A central **MICROCOMPUTER** (23A) is connected to several control circuits:
 - AGC CONTROL CIRCUIT** (23a) connected to the AGC circuit (8).
 - ELECTRONIC SHUTTER CONTROL CIRCUIT** (23b) connected to the image-sensing-device control circuit (6).
 - IRIS CONTROL CIRCUIT** (23c) connected to an **IRIS DRIVE CIRCUIT** (14).
 - AGC-DATA HOLDING CIRCUIT** (231), **ELECTRONIC-SHUTTER-DATA HOLDING CIRCUIT** (232), and **IRIS-DATA HOLDING CIRCUIT** (233) are data holding circuits connected to their respective control circuits.
 - 23d: DATA HOLDING CIRCUIT** is also connected to the microcomputer.
- Exposure Control and Correction:**
 - The **AUTOMATIC EXPOSURE CONTROL CIRCUIT** (23e) receives input from the camera-signal processing circuit (109b) and the AGC control circuit (23a). It outputs to an **AE DETECTOR CIRCUIT** (23g) and an **EXPOSURE CORRECTION RANGE CONTROL CIRCUIT** (23f).
 - The **AE DETECTOR CIRCUIT** (23g) is connected to a **PHOTOMETRIC AREA CONTROL CIRCUIT** (23h).
 - The **EXPOSURE CORRECTION RANGE CONTROL CIRCUIT** (23f) controls two switches: **SW1** and **SW3**.
 - SW1** is connected to the **EXPOSURE STATE DISCRIMINATING CIRCUIT** (23i).
 - SW3** is connected to the **EXPOSURE CORRECTION EXECUTION KEY** (22).
 - The **EXPOSURE STATE DISCRIMINATING CIRCUIT** (23i) outputs to the **EXPOSURE CORRECTION RANGE CONTROL CIRCUIT** (23f).
- Output and User Interface:**
 - The **EXPOSURE CORRECTION RANGE CONTROL CIRCUIT** (23f) outputs to a **DISPLAY CIRCUIT** (19).
 - The **DISPLAY CIRCUIT** (19) is connected to a **VIDEO RECORDER** (10) and an **EVF** (11).
 - The **EXPOSURE CORRECTION EXECUTION KEY** (22) is connected to the **EXPOSURE CORRECTION RANGE CONTROL CIRCUIT** (23f) and the **EXPOSURE STATE DISCRIMINATING CIRCUIT** (23i).
 - The **EXPOSURE STATE DISCRIMINATING CIRCUIT** (23i) also outputs to the **EXPOSURE CORRECTION RANGE CONTROL CIRCUIT** (23f).

The diagram illustrates a video camera system with exposure correction. The main components and their interconnections are as follows:

- 1**: LENS
- 2**: Image Sensing Device (part of 3)
- 3**: IMAGE SENSING DEVICE
- 4**: I/O (Input/Output)
- 5**: I/O (Input/Output)
- 6**: IMAGE-SENSING-DEVICE CONTROL CIRCUIT
- 7**: S/H CIRCUIT (Sample and Hold)
- 8**: AGC CIRCUIT (Automatic Gain Control)
- 9**: CAMERA-SIGNAL PROCESSING CIRCUIT
- 10**: VIDEO RECORDER
- 11**: EVF (Electronic Viewfinder)
- 12**: + (Addition symbol)
- 13**: - (Subtraction symbol)
- 14**: IRIS DRIVE CIRCUIT
- 15**: IRIS CONTROL CIRCUIT
- 16**: ELECTRONIC SHUTTER CONTROL CIRCUIT
- 17**: AE DETECTOR CIRCUIT
- 18a**: AGC CONTROL CIRCUIT
- 18b**: ELECTRONIC SHUTTER CIRCUIT
- 18c**: IRIS CONTROL CIRCUIT
- 18d**: DATA HOLDING CIRCUIT
- 18e**: AUTOMATIC EXPOSURE CONTROL CIRCUIT
- 18f**: EXPOSURE-STATE DISCRIMINATING CIRCUIT
- 18g**: EXPOSURE CORRECTION RANGE CONTROL CIRCUIT
- 18h**: EXPOSURE CORRECTION RANGE CONTROL CIRCUIT
- 18i**: EXPOSURE CORRECTION RANGE CONTROL CIRCUIT
- 18j**: EXPOSURE CORRECTION RANGE CONTROL CIRCUIT
- 18k**: EXPOSURE CORRECTION RANGE CONTROL CIRCUIT
- 18l**: EXPOSURE CORRECTION RANGE CONTROL CIRCUIT
- 18m**: EXPOSURE CORRECTION RANGE CONTROL CIRCUIT
- 18n**: EXPOSURE CORRECTION RANGE CONTROL CIRCUIT
- 18o**: EXPOSURE CORRECTION RANGE CONTROL CIRCUIT
- 18p**: EXPOSURE CORRECTION RANGE CONTROL CIRCUIT
- 18q**: EXPOSURE CORRECTION RANGE CONTROL CIRCUIT
- 18r**: EXPOSURE CORRECTION RANGE CONTROL CIRCUIT
- 18s**: EXPOSURE CORRECTION RANGE CONTROL CIRCUIT
- 18t**: EXPOSURE CORRECTION RANGE CONTROL CIRCUIT
- 18u**: EXPOSURE CORRECTION RANGE CONTROL CIRCUIT
- 18v**: EXPOSURE CORRECTION RANGE CONTROL CIRCUIT
- 18w**: EXPOSURE CORRECTION RANGE CONTROL CIRCUIT
- 18x**: EXPOSURE CORRECTION RANGE CONTROL CIRCUIT
- 18y**: EXPOSURE CORRECTION RANGE CONTROL CIRCUIT
- 18z**: EXPOSURE CORRECTION RANGE CONTROL CIRCUIT
- 19**: DISPLAY CIRCUIT
- 20**: PHOTOMETRIC AREA CONTROL CIRCUIT
- 21**: TWO-DIMENSIONAL POSITION SELECTING CIRCUIT
- 22**: EXPOSURE CORRECTION EXECUTION KEY

FIG. 13

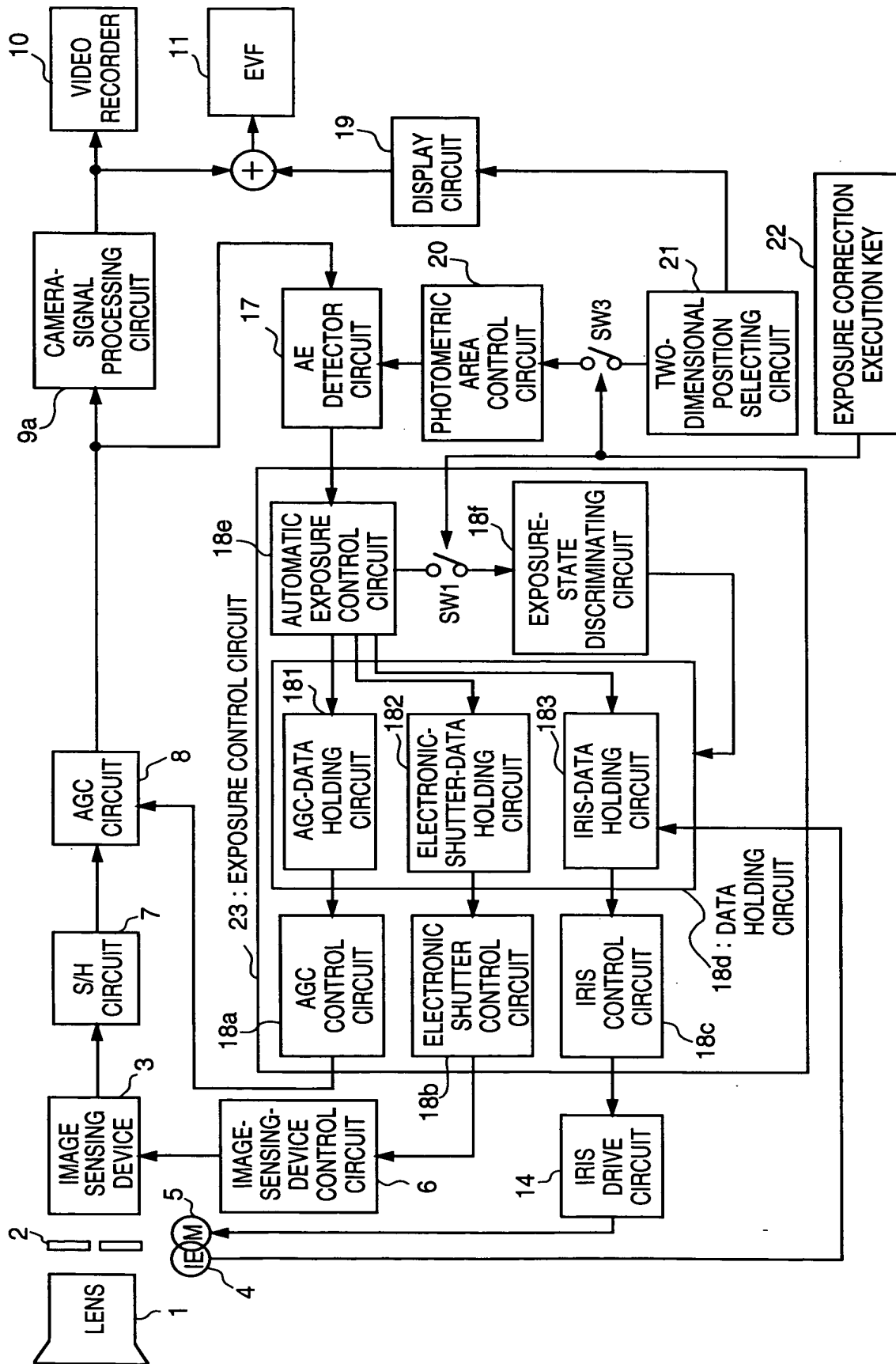
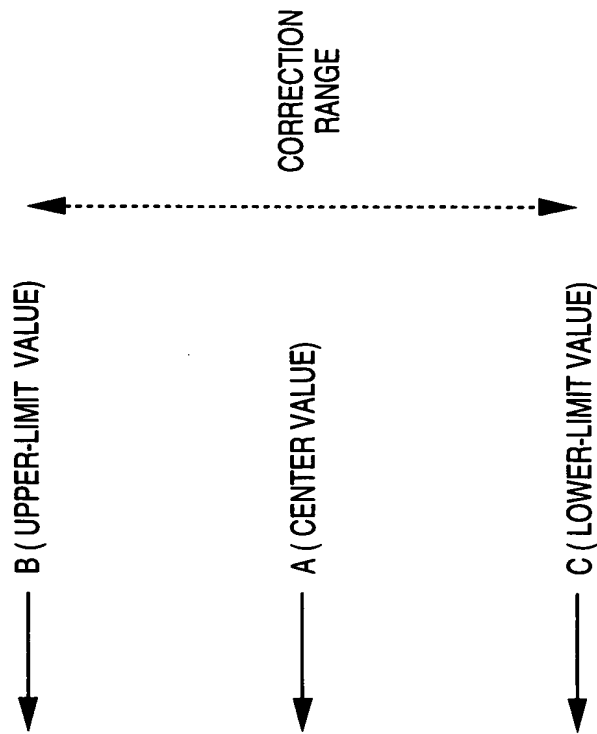


FIG. 14

DATA No.	IRIS DATA	AGC DATA	SHUTTER DATA
data1	OPEN	MAX	1/60
data2		18dB	
data3		12dB	
data4		6dB	
data5		0dB	
data6	F2.8		
data7	F4		
data8	F5.6		
data9	F8		
data10	F11		
data11	F22		
data12	F32		
data13			1/100
data14			1/250
data15			1/500
data16			1/1000
data17	CLOSE		



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